

Cense Winter School 2025

A conference to shape the future of semiconductor technology
CENTRE FOR NANO SCIENCE AND ENGINEERING
Indian Institute of Science, Bengaluru-560012
Tel: 080-2293 3542 Email: workshop.cense@iisc.ac.in



Terms and Conditions

By enrolling in this program, you agree to adhere to these terms and conditions.

- The curriculum of the Winter School 2025 closely resembles that of the Summer School 2025, which took place in June 2025.
- Access to the program materials and sessions is limited to enrolled participants only. Sharing with others is strictly prohibited.
- The application fee must be paid in full before the start date. Due to the digital nature of the program, all fees are non-refundable once access is granted.
- The program will run for ten consecutive days, with specified session times. It is the participant's responsibility to attend all sessions. Sessions may be recorded for later access; however, live attendance is encouraged for optimal learning and participation.
- Participants are responsible for ensuring they have the necessary technical setup, including a reliable internet connection, to access program sessions. IISc CeNSE is not liable for technical difficulties caused by participant equipment.
- Participants are expected to engage respectfully in all program sessions, online discussions and test.
- Privacy and Confidentiality- Personal information collected during registration will be used solely for the purpose of program administration and communication. We value your privacy and will not share your information with third parties.
- IISc CeNSE reserves the right to make changes to the program schedule, content, or instructors as necessary. In case of cancellation of the program, a full refund will be issued.
- All materials provided in the program, including but not limited to videos, presentations, and handouts, are the intellectual property of IISc CeNSE. Faculty members reserve the right to decide whether to share their presentations and study materials. These materials are for personal use only and may not be copied, distributed, or used for commercial purposes without prior permission.



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Note:

Upon successful completion of the program, participants will be eligible for a certificate of participation issued by CeNSE, provided they meet the performance and attendance requirements.

The primary goal of this Winter school is to foster interest in semiconductor fabrication. Evaluations are designed to motivate attendance and engagement, rather than create a competitive environment. Therefore, we will not release any merit list, as we believe that could detract from the collaborative learning experience.

Assessments are intended to encourage active participation and engagement, not to differentiate between closely scored performances (e.g., 80 vs. 85). As such, the focus is on the overall learning experience rather than minor score distinctions.

In line with our philosophy of promoting an inclusive learning environment, individual scores will not be disclosed. This policy supports confidentiality, reduces bias, and ensures a consistent experience for all participants.

Participants will receive one of two types of certificates, depending on their performance:

Certificate of Distinction for those who have performed exceptionally well.

Certificate of Participation for most other participants who met the engagement requirements.

Note: Certificates will be withheld only in rare cases where participants attended very few sessions and achieved extremely low scores.